

Ⓢ

**EUROPEAN PATENT APPLICATION**

Ⓣ<sup>1</sup> Application number: 88303929.9

Ⓢ<sup>1</sup> Int. Cl.<sup>4</sup>: **C25D 5/02 , C25D 7/04**

Ⓢ<sup>2</sup> Date of filing: 29.04.88

Ⓢ<sup>3</sup> Priority: 01.05.87 JP 106347/87

Ⓢ<sup>4</sup> Date of publication of application:  
09.11.88 Bulletin 88/45

Ⓢ<sup>4</sup> Designated Contracting States:  
**DE FR GB**

Ⓢ<sup>7</sup> Applicant: Oki Electric Industry Company,  
Limited  
7-12, Toranomom 1-chome Minato-ku  
Tokyo 105(JP)

Ⓢ<sup>7</sup> Inventor: Yamato, Osamu  
Oki Electric Ind. Co.,Ltd. 7-12 Toranomom  
1-chome  
Minato-ku Tokyo(JP)  
Inventor: Takino, Tetsuji  
Oki Electric Ind. Co.,Ltd. 7-12 Toranomom  
1-chome  
Minato-ku Tokyo(JP)

Ⓢ<sup>7</sup> Representative: Read, Matthew Charles et al  
Venner Shipley & Co. 368 City Road  
London EC1V 2QA(GB)

Ⓢ<sup>4</sup> Dielectric block plating process and a plating apparatus for carrying out the same.

Ⓢ<sup>7</sup> A dielectric block plating process and an apparatus for carrying out the same. An electrolytic solution is circulated through a plating tank (401) in a substantially uniform flow in practically all over the interior of the plating tank (401) to form conductive layers over predetermined areas of a ground conductive layers formed on the surface of a dielectric block (100) having through holes (111 to 116) extending from the top surface to the bottom surface thereof. The dielectric block (100) is immersed and held in the electrolytic solution contained in the plating tank (401) so that the respective center axes of the through holes (111 to 116) extend of the dielectric block (100) extend in parallel to the direction of flow of the electrolytic solution.

**EP 0 290 210 A2**

**BEST AVAILABLE COPY**

## DIELECTRIC BLOCK PLATING PROCESS AND A PLATING APPARATUS FOR CARRYING OUT THE SAME

The present invention relates to a dielectric block plating process for forming a plated layer through an electrolytic plating process over the surface of a dielectric block having through holes extending from the bottom surface to the top surface thereof, and a plating apparatus for implementing the dielectric block plating process.

Generally, the dielectric block plating process for forming a conductive layer comprises the following steps.

1) washing and roughening the surface of a dielectric block made of ceramic material comprised of  $\text{TiO}_2$  and  $\text{BaO}$ .

2) coating the surface of the dielectric block with a ground conductive layer through an electroless plating process,

3) coating predetermined portion not to be coated with an upper conductive layer on the surface of the dielectric block with a resist material,

4) coating the surface of the dielectric block with an upper conductive layer through an electroplating process, and

5) removing the resist material and the ground conductive layer coated with the resist material.

When the dielectric block is plated through the foregoing dielectric block plating process to manufacture a dielectric filter having inner conductors in the through holes and an outer conductor at the surface thereof, the electrolytic solution is liable to stagnate in the through holes of the dielectric block and hence, the surfaces of the through holes may be coated with inner conductive layers having an insufficient thickness. The electric characteristics of such dielectric filter containing inner conductive layers (inner conductors) having an insufficient thickness thereof, do not meet desired electric characteristics.

There is a dielectric block plating process invented to solve the foregoing problem in Japanese Patent Laid-Open Publication No. 59-185795. This conventional dielectric block plating process requires a plurality of electrolytic solution injecting means to be disposed respectively adjacent and opposite to the through holes of a dielectric block. Accordingly, this dielectric block plating process requires the dielectric block being set exactly at a predetermined position adjacent to the electrolytic solution injecting means.

Another conventional dielectric block plating process employs auxiliary anodes, which are respectively inserted in the through holes of a dielectric block to form inner conductors having a sufficient thickness.

Accordingly, it is an object of the present in-

vention to provide a dielectric block plating process and a dielectric block plating apparatus for implementing the dielectric block plating process capable of forming conductive layers having a uniform thickness over the surfaces of a dielectric block.

It is another object of the present invention to provide a dielectric block plating apparatus having a simple construction.

It is a further object of the present invention to provide a dielectric block plating apparatus capable of satisfactorily coating the surfaces of the through holes of a dielectric block with conductive layers without exactly positioning the through holes respectively adjacent and opposite to each of electrolytic solution injecting means for injecting the electrolytic solution into the through holes of the dielectric block, and inserting auxiliary anodes in each of the through holes of the dielectric block.

It is still a further object of the present invention to provide a dielectric block plating apparatus suitable for automation.

In one aspect of the present invention, a dielectric block plating process for forming a conductive layer through an electroplating process over the surfaces of a dielectric block having through holes extending from the top surface to the bottom surface thereof and coated with a ground conductive layer formed through an electroless plating process in a plating tank filled with an electrolytic solution comprises steps of:

1) immersing the dielectric block in the electrolytic solution,

2) causing the electrolytic solution to flow in the direction of the center axes of the through holes,

3) applying a negative voltage to the ground conductive layer,

4) applying a positive voltage to the electrolytic solution, and

5) taking out the dielectric block from the electrolytic solution.

In another aspect of the present invention, a dielectric block plating apparatus for forming conductive layers over the surfaces of a dielectric block having through holes extending from the top surface to the bottom surface thereof and coated with a ground conductive layer formed through an electroless plating process in a plating tank filled with an electrolytic solution comprises:

1) dielectric block holding means for holding the dielectric block in the electrolytic solution,

2) means for causing the electrolytic solution to flow in the direction of the center axis of the through holes,

3) means for controlling the electric solution to flow in the direction of the center axis of the through holes,

4) means for applying a positive voltage to the electrolytic solution, and

5) means for applying a negative voltage to the ground conductive layer.

The above and other objects, features and advantages of the present invention will become apparent from the following description taken in conjunction with the accompanying drawings.

Fig. 1 is a perspective view of a dielectric block;

Fig. 2 is a sectional view taken on line 103-103 in Fig. 1;

Fig. 3 is a sectional view of a dielectric block plating apparatus, in a preferred embodiment, according to the present invention;

Fig. 4 is a fragmentary cutaway perspective view of the dielectric block plating apparatus of Fig. 3;

Figs. 5 and 6 are perspective views of assistance in explaining the construction of an electrolytic solution supply unit incorporated into the dielectric block plating apparatus of Fig. 3;

Fig. 7 is a perspective view of a holding device for holding a dielectric block; and

Fig. 8 is a side elevation of the holding device holding a dielectric block.

Referring to Figs. 1 and 2, a dielectric block 100 formed of a ceramic material containing  $\text{TiO}_2$  and  $\text{BaO}$  has through holes 111 to 116 extending from the top surface to the bottom surface thereof. Conductive collared areas 121-126 for adjusting the capacitance of the dielectric block 100 are formed in the top surface of the dielectric block respectively around the edges of the through holes 111 to 116. Also formed in the top surface of the dielectric block 100 are electrodes 141. Indicated at 151 is a nonplated area in which no conductive layer is formed. An outer conductor 131 is formed in the side surfaces of the dielectric block 100. As best shown in Fig. 2, inner conductors 121 to 126 are formed respectively over the surfaces of the through holes 111 to 116. The inner conductors 121 to 126 are connected electrically to the outer conductor 131 by a bottom conductor 161.

Referring to Figs. 3 and 4 showing a dielectric block plating apparatus, in a preferred embodiment, according to the present invention. A plating tank 401 is filled with an electrolytic solution. The dielectric block 100 is immersed in the electrolytic solution in the plating tank 401 for electrolytic plating. The electrolytic solution is pumped for circulation through the plating tank 401 by a pump 402. The electrolytic solution, for example, for silver plating contains  $\text{KAg}(\text{CN})_2$  and  $\text{KCN}$ . The electrolytic solution pumped by the pump 402 is supplied

into the plating tank 401 through a supply pipe 403, such as a vinyl chloride pipe, connected to a pipe joint 408 connected to the bottom of the plating tank 401 so that the electrolytic solution will overflow the plating tank 401. The electrolytic solution overflows the plating tank 401 into a sump 404 formed around the plating tank 401 between the walls of the plating tank 401 and an external casing. A return pipe 405 has one end connected to the bottom of the external casing and the other end connected to the pump 402. The overflow electrolytic solution contained in the sump 404 is returned through the return pipe 405 to the pump 402. Preferably, a filter unit 407 is provided in the return pipe 405.

Flow control devices for forming a uniform flow of the electrolytic solution within the plating tank 401 will be described hereinafter.

In Figs. 3 and 4, the flow control device is embodied in a flow control plate 409 having small perforations 410. The perforations are distributed over the entire area of the flow control plate 409. The electrolytic solution supplied through the supply pipe 403 into the plating tank 401 is distributed uniformly by the flow control plate 409, so that a substantially uniform flow of the electrolytic solution is formed within the plating tank 401.

In Fig. 5, the flow control device is embodied in a manifold formed by branching the free end of the supply pipe 403 into a plurality of branches on the bottom wall of the plating tank 401. A plurality of through holes 410 are formed in the manifold to spout the electrolytic solution uniformly therethrough into the plating tank 401.

In Fig. 6, the flow control device is embodied in a zigzag pipe formed by bending the free end of the supply pipe 403 in a zigzag shape on the bottom wall of the plating tank 401. A plurality of through holes 410 are formed in the zigzag pipe to spout the electrolytic solution therethrough uniformly into the plating tank 401.

The perforated plate, the manifold and the zigzag pipe are equivalent to each other in the effect of forming a uniform flow of the electrolytic solution within the plating tank 401.

Anodes 420 for applying a positive voltage to the electrolytic solution are arranged within the plating tank 401. Each anode 420 is, for example of silver plating, a rectangular plate formed of pure silver. The anodes 420 are fixed to an anode holding structure 421, such as a net or a cage, formed of Ti as shown in Figs. 3 and 4. The anode holding structure 421 holding the anodes 420 is fixedly placed within the plating tank 401.

Referring to Figs. 7 and 8, a holding device 430 for holding the dielectric block 100 within the plating tank 401 to immerse the dielectric block 100 in the electrolytic solution is formed of a ma-

terial which will not dissolve in the electrolytic solution and in a solution for removing a material plated over the surface of the holding device 430, for example, a stainless steel material or piano wires. The holding device 430 comprises a frame 701, a pair of grippers 702 for gripping the dielectric block 100, and a spring 703 biasing the pair of grippers 702 so as to grip the dielectric block 100 therebetween. The pair of grippers 702 are supported on the frame 701. When the pair of grippers 702 are formed of a resilient material in a shape capable of holding the dielectric block 100 therebetween, the springs 703 need not be provided. The respective pairs of grippers 702 are closed when no dielectric block is held therebetween. In holding the dielectric block 100, the pair of grippers 702 are opened to receive the dielectric block 100 therebetween as shown in Fig. 8. The pair of grippers 702 are connected electrically to the frame 701, so a voltage applied to the frame 701 can be applied to the ground conductive layer over the surfaces of the dielectric block 100.

Thus, a positive voltage is applied to the anodes 420 and a negative voltage is applied to the ground conductive layer formed over the surfaces of the dielectric block 100. Thereby an upper conductive layer is formed over the predetermined area of the ground conductive layer.

Although the invention has been described in its preferred form with a certain degree of particularity, obviously many changes and variations are possible therein. It is therefore to be understood that the present invention may be practiced otherwise than specifically described herein without departing from the scope and spirit thereof.

## Claims

1. A dielectric block plating process for forming a conductive layer through an electroplating process over the surface of a dielectric block (100) having through holes (111 to 116) extending from the top surface to the bottom surface thereof and coated with a ground conductive layer formed through an electroless plating process in a plating tank (401) filled with an electrolytic solution, said dielectric block plating process comprising steps of:

(1) immersing the dielectric block (100) in the electrolytic solution contained in the plating tank (401);

(2) circulating the electrolytic solution through the plating tank (401) so that the electrolytic solution will flow in the direction of the center axes of the through holes (111 to 116);

(3) applying a negative voltage to the ground conductive layer;

(4) applying a positive voltage to the electrolytic solution; and

(5) taking out the dielectric block (100) from the electrolytic solution.

2. A dielectric block plating process according to Claim 1, wherein the flow of the electrolytic solution within the plating tank (401) is controlled so as to be substantially uniform.

3. A dielectric block plating process according to Claim 2, wherein the electrolytic solution is supplied into the plating tank (401) so as to overflow the plating tank (401) into a sump (404) formed around the plating tank (401), and the overflowed electrolytic solution is discharged from the plating tank (401).

4. A dielectric block plating process according to Claim 3, wherein the electrolytic solution discharged from the plating tank (401) is supplied again into the plating tank (401).

5. A dielectric block plating process according to Claim 4, the electrolytic solution is filtered.

6. A dielectric block plating apparatus for forming conductive layers through an electroplating process over the surface of a dielectric body (100) having through holes (111 to 116) extending from the top surface to the bottom surface thereof and coated with a ground conductive layer formed through an electroless plating process in a plating tank (401) filled with an electrolytic solution, said dielectric body plating apparatus comprising:

(1) holding means for holding the dielectric block (100) within the electrolytic solution;

(2) electrolytic solution circulating means for circulating the electrolytic solution under pressure through the plating tank (401);

(3) means for controlling the flow of the electric solution so as to flow in accordance with the direction of the axes of the through holes (111 to 116);

(4) voltage applying means for applying a negative voltage to the ground conductive layer formed on the dielectric block (100); and

(5) voltage applying means for applying a positive voltage to the electrolytic solution.

7. A dielectric block plating apparatus according to Claim 6, further comprising a sump (404) means for containing the overflowed electrolytic solution overflowed said plating tank (401).

8. A dielectric block plating apparatus according to Claim 7, further comprising discharge means for discharging the overflowed electrolytic solution contained in the sump (404) means from said plating tank (401).

9. A dielectric block plating apparatus according to Claim 8, further comprising means for supplying the electrolytic solution discharged from said plating tank (401) again into said plating tank (401).

10. A dielectric block plating apparatus according to Claim 9, further comprising filtering means for filtering the electrolytic solution.

11. A dielectric block plating apparatus according to Claim 10, further comprising a plate (409) having a plurality of perforations (410), provided within the plating tank (401). 5

12. A dielectric block plating apparatus according to Claim 10, wherein said electrolytic solution supplying means comprises a pipe (403) disposed on the bottom wall of the plating tank (401) and having a plurality of small through holes (410) for spouting the electrolytic solution therethrough. 10

13. A dielectric block plating apparatus according to Claim 12, wherein said pipe (403) is branched in a plurality of branch pipes on the bottom wall of the plating tank (401). 15

14. A dielectric block plating apparatus according to Claim 12, wherein said pipe (403) extends in a zigzag shape on the bottom wall of the plating tank (401). 20

15. A dielectric block plating process wherein a dielectric block (100) provided with a ground conductive layer and at least one through hole (111 to 116) is immersed in electrolyte solution contained in a plating tank (401), an electrical potential difference is applied between the block and an electrode (420) in the tank, and the electrolytic solution is caused to circulate characterised in that the flow of electrolyte is arranged to be substantially uniform over at least a major portion of the tank and in the direction of the hole (111 to 116) through the block. 25 30

35

40

45

50

55

Fig. 1

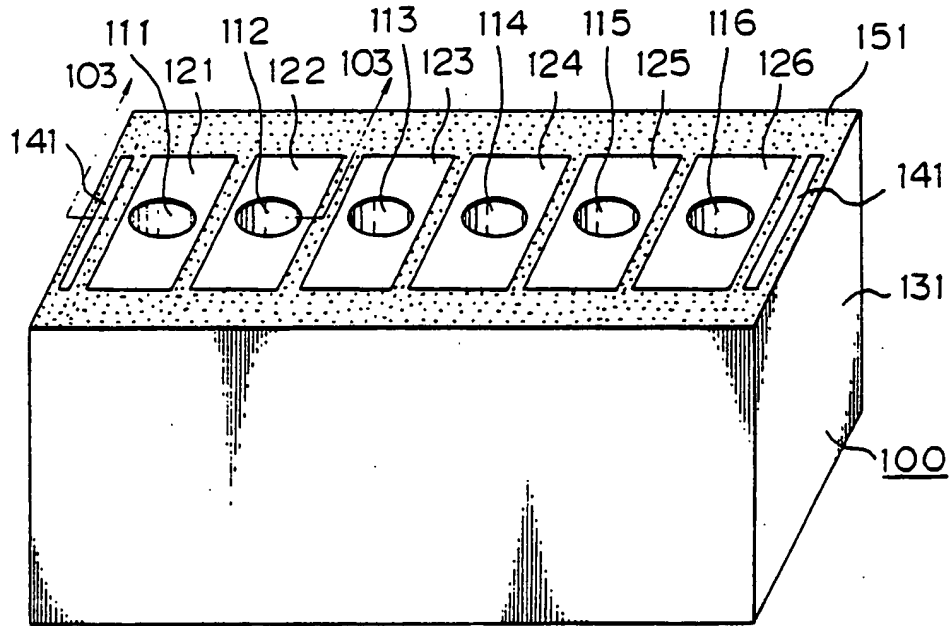


Fig. 2

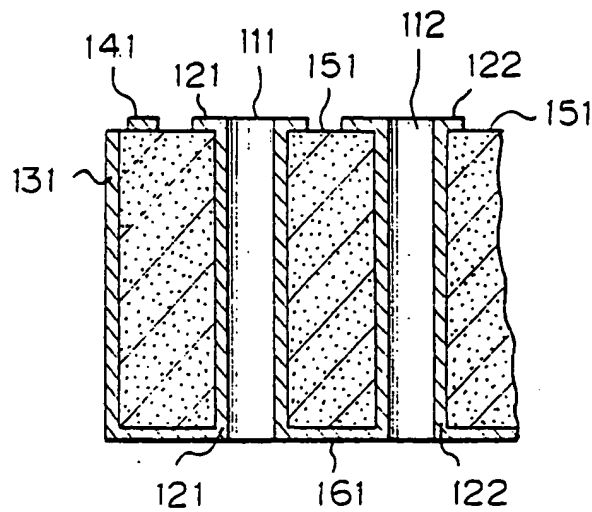


Fig. 3

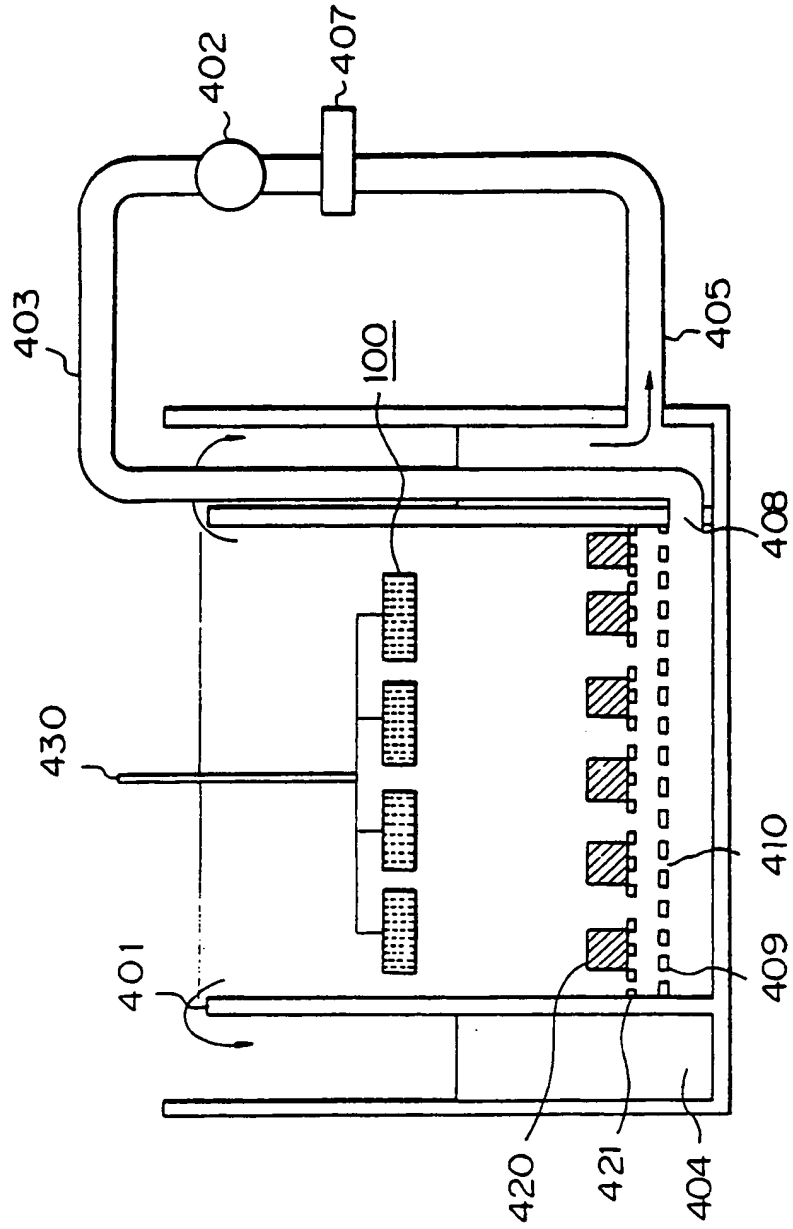


Fig. 4

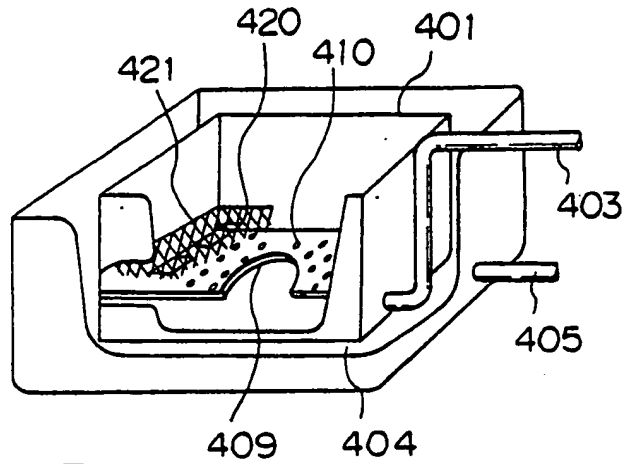


Fig. 5

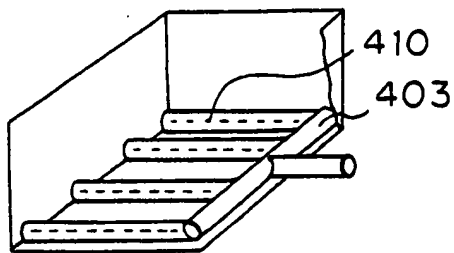


Fig. 6

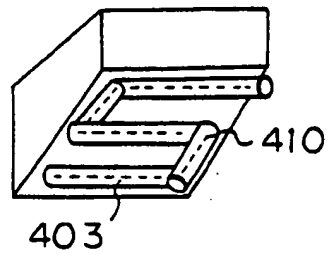


Fig. 7

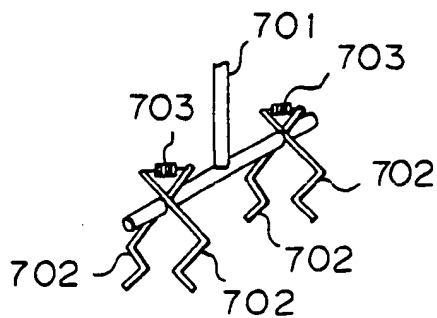
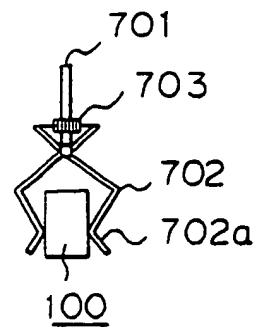


Fig. 8





12

# **EUROPEAN PATENT APPLICATION**

21 Application number: 88303929.9

51 Int. Cl.4: **C 25 D 5/02**  
**C 25 D 7/04**

22 Date of filing: 29.04.88

30 Priority: 01.05.87 JP 106347/87

43 Date of publication of application:  
09.11.88 Bulletin 88/45

84 Designated Contracting States: DE FR GB

88 Date of deferred publication of search report:  
01.02.89 Bulletin 89/05

71 Applicant: Oki Electric Industry Company, Limited  
7-12, Toranomom 1-chome Minato-ku  
Tokyo 105 (JP)

72 Inventor: Yamato, Osamu  
Oki Electric Ind. Co., Ltd. 7-12 Toranomom 1-chome  
Minato-ku Tokyo (JP)

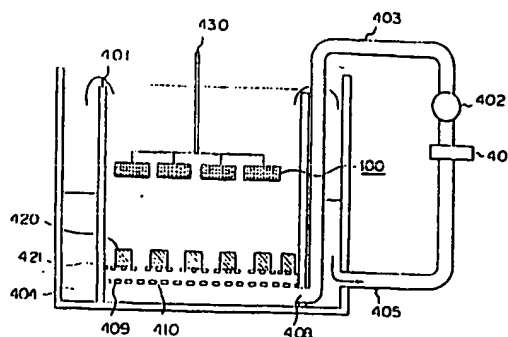
Takino, Tetsuji  
Oki Electric Ind. Co., Ltd. 7-12 Toranomom 1-chome  
Minato-ku Tokyo (JP)

74 Representative: Read, Matthew Charles et al  
Venner Shipley & Co. 368 City Road  
London EC1V 2QA (GB)

54 Dielectric block plating process and a plating apparatus for carrying out the same.

57 A dielectric block plating process and an apparatus for carrying out the same. An electrolytic solution is circulated through a plating tank (401) in a substantially uniform flow in practically all over the interior of the plating tank (401) to form conductive layers over predetermined areas of a ground conductive layers formed on the surface of a dielectric block (100) having through holes (111 to 116) extending from the top surface to the bottom surface thereof. The dielectric block (100) is immersed and held in the electrolytic solution contained in the plating tank (401) so that the respective center axes of the through holes (111 to 116) extend of the dielectric block (100) extend in parallel to the direction of flow of the electrolytic solution.

Fig. 3





European Patent  
Office

# EUROPEAN SEARCH REPORT

Application Number

EP 88 30 3929

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.4)
A,D	PATENT ABSTRACTS OF JAPAN, vol. 9, no. 41, (C-267)[1764], 21st February 1985; & JP-A-59 185 795 (OKI DENKI KOGYO K.K.) 22-10-1984 ---		C 25 D 5/02 C 25 D 7/04
A	IBM TECHNICAL DISCLOSURE BULLETIN, vol. 26, no. 7A, December 1983, pages 3143-3144, New York, US; L.R. DALEY et al.: "Flow distributor for plating tank" ---		
A	DE-A-1 796 178 (PHILIPS) -----		
			TECHNICAL FIELDS SEARCHED (Int. Cl.4)
			C 25 D 5/02 C 25 D 5/08 C 25 D 7/04 H 01 H 1/00
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 10-11-1988	Examiner VAN LEEUWEN R.H.
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons ..... & : member of the same patent family, corresponding document	

**This Page is Inserted by IFW Indexing and Scanning  
Operations and is not part of the Official Record**

## **BEST AVAILABLE IMAGES**

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images include but are not limited to the items checked:

- ☒ **BLACK BORDERS**
- ☐ **IMAGE CUT OFF AT TOP, BOTTOM OR SIDES**
- ☐ **FADED TEXT OR DRAWING**
- ☐ **BLURRED OR ILLEGIBLE TEXT OR DRAWING**
- ☐ **SKEWED/SLANTED IMAGES**
- ☐ **COLOR OR BLACK AND WHITE PHOTOGRAPHS**
- ☐ **GRAY SCALE DOCUMENTS**
- ☐ **LINES OR MARKS ON ORIGINAL DOCUMENT**
- ☐ **REFERENCE(S) OR EXHIBIT(S) SUBMITTED ARE POOR QUALITY**
- ☐ **OTHER:** \_\_\_\_\_

**IMAGES ARE BEST AVAILABLE COPY.**

**As rescanning these documents will not correct the image problems checked, please do not report these problems to the IFW Image Problem Mailbox.**